	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	1	"6594799".pn.	USPAT	2006/09/07 09:57
2	BRS	L2	1	"6134516".pn.	USPAT	2006/09/07 09:57
3	BRS	L3	1	"6134516".pn. and output	USPAT	2006/09/07 09:58
4	BRS	L4	1	"6292765".pn. and (output same file$2)	USPAT	2006/09/07 10:00
5	BRS	L5	0	(simulation same (outfile adj file))	USPAT	2006/09/07 09:59
6	BRS	L6	0	(simulation same (outfile adj file))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	2006/09/07 09:59
7	BRS	L7	0	(simulation and (outfile adj file))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	2006/09/07 09:59
8	BRS	L8	0	"6594799".pn. and (output same file$2)	USPAT	2006/09/07 10:00
9	BRS	L9	824	simulation same (output same file$2)	USPAT	2006/09/07 10:00
10	BRS	L10	977	simulation same (output same file$2) same circuit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	2006/09/07 10:01
11	BRS	L11	246	simulation same (output same file$2) same circuit same first and second	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	2006/09/07 10:01

	Type	L #	Hits	Search Text	DBs	Time Stamp
12	BRS	L12	88	simulation same (output same file$2) same (circuit adj design)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	2006/09/07 10:04
13	BRS	L13	36	(simulation adj output adj file)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	2006/09/07 10:04
14	BRS	L14	27	(simulation adj output adj file) and circuits	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	2006/09/07 10:04
15	BRS	L15	10	(simulation adj output adj file) same circuits	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	2006/09/07 10:48
16	BRS	L16	968	(simulation adj engine$2)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	2006/09/07 10:48

	Type	L #	Hits	Search Text	DBs	Time Stamp
17	BRS	L17	183	(simulation adj engine$2) same circuit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	2006/09/07 10:49